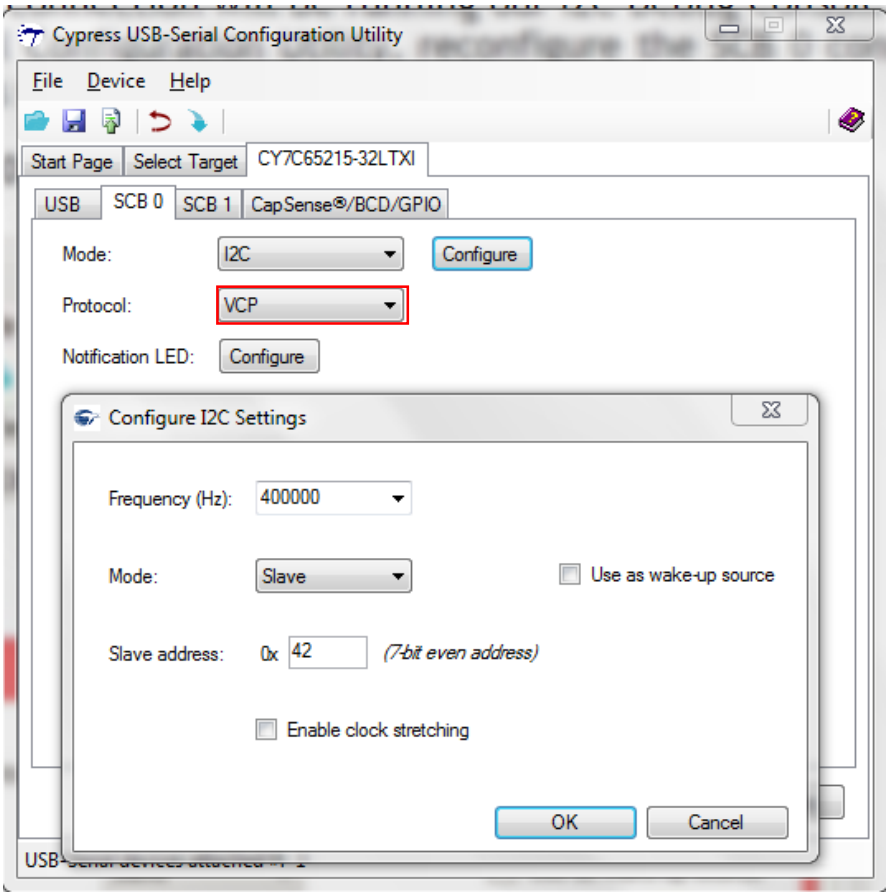
ERRATA TO THE SUPERSPEED DEVICE DESIGN BY EXAMPLE BOOK V1.2 CODE EXAMPLES

1. Configure I2C Debug Console for Dual Console Project (Page 103)

Please follow below steps to configure the USB UART chip as I2C Debug Console.

- Open the Cypress USB Configuration Utility attached in this folder.
- Re-configure the SCB0 connection to support I2C Slave as shown in the below Figure.



- Make sure you choose the protocol as **VCP (Virtual COM port)** as highlighted in red box in the above Figure so that it will bind to the Windows CDC driver.
- Click on OK once the configuration is done.
- Click on Program button and then Disconnect button to reset the device so that the new settings will take effect.

2. Running SPI_Example (Page 108)

- Program SPI_Slave.xsvf file into the CPLD to configure the CPLD as an SPI Slave. The CPLD code only takes care of toggling the LEDs based on the value sent by FX3 through SPI interface. The user can develop FX3 and CPLD code for SPI flash access.

3. Interconnecting two SuperSpeed Explorer Boards (Page 220)

- Download both the FX3 kits with MasterFIFO and SLaveFIFO images respectively. Run four streamer instances to transfer data in both directions simultaneously as shown in the Figure below.

